Appl. No. 10/719,357 Amdt. dated December 27, 2004 Reply to Office Action of November 4, 2004

## **Amendments to the Claims:**

Claims 14-16 are pending in this application. This listing of claims will replace all prior versions, and listings of claims in the application:

## **Listing of Claims:**

- 14. (original) An integrated circuit, comprising:
- a) a standard cell array comprising rows of cells having a row pitch; and
- b) a MACRO, embedded within the standard cell array, comprising rows of cells having pitch substantially equal to the row pitch.
  - 15. (Currently amended) An integrated circuit, comprising:
- a) a standard cell array region, comprising rows of cells having a row pitch;
  - b) a pair of power conductors extending across each row; and
- c) a MACRO region <u>embedded within said standard cell array region</u>, comprising rows of cells having a pitch equal to said row pitch.
  - 16. (original) An integrated circuit, comprising:
    - a) cells of a standard cell array;
- b) cells of a MACRO, embedded in the standard cell array, having a row pitch substantially the same as the standard cell array and
  - c) parallel power conductors running adjacent all of said cells.